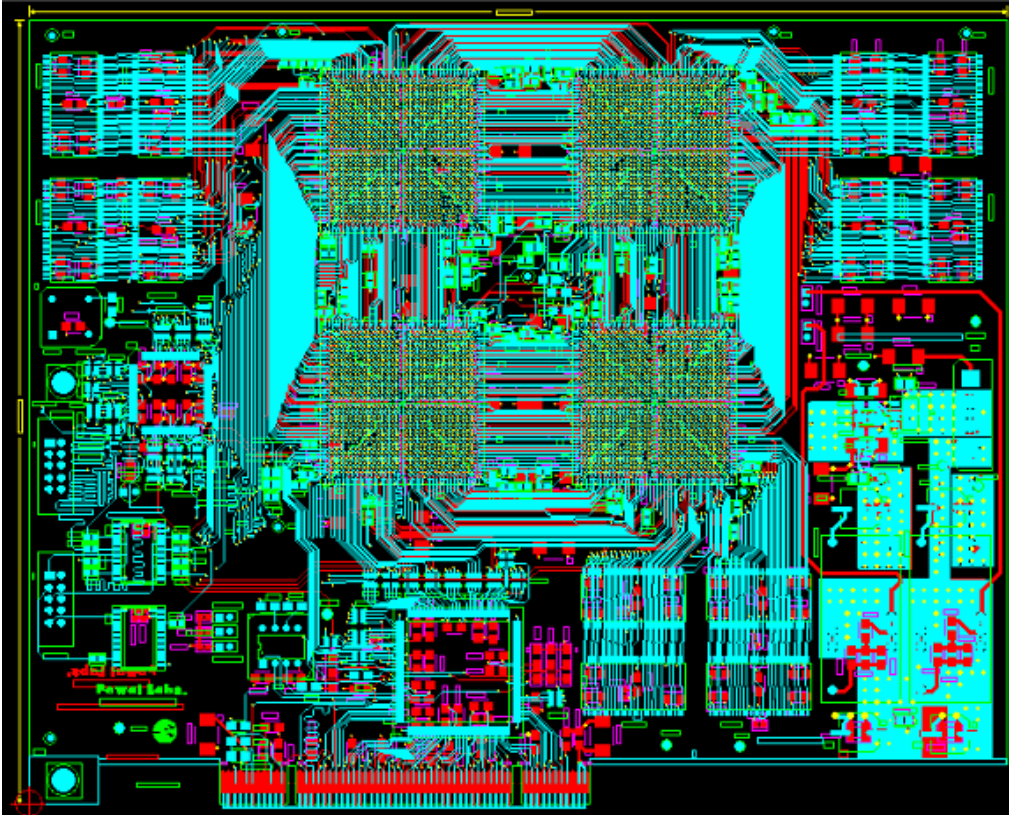


PCB DESIGN CASE STUDY

PCB Name : ASIC Accelerator Emulator:

- It is a PCI Based Emulator Card with 20 million FPGA gates. Working at 33 MHz.
- **Major components** - 4 Spartan-3 FPGA of 1156 pins FBGA (Fine pitch BGA), Memories..
- **Board Size** = 20.95 cm x 16.8 cm = 352 sq cm
- **Layers** - 12
- **Components** - 504 of which 220 were from top side and 284 from bottom side.
- Number of equivalent 16 pin ICs - 428.
- Total No. of Connections - 5375.
- No. of Vias - 4754.



ACTUAL PCB PHOTOGRAPH

